

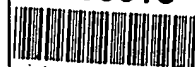
1c857 U.S. PTO  
09/923207



438	Class	287	Subclass
ISSUE CLASSIFICATION			

PATENT NUMBER

6583015



6583015

# U.S. UTILITY Patent Application

O.I.P.E. <i>KS</i> SCANNED <i>HC1</i> O.A. <i>MT</i>	PATENT DATE JUN 24 2003
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APPLICATION NO. 09/923207	CONT/PRIOR D	CLASS 438	SUBCLASS 287	ART UNIT 2811 2812	EXAMINER <i>Lindsay</i>
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APPLICANTS  
Eugene Fitzgerald  
Richard Hammond  
Matthew Currie

TITLE  
Gate technology for strained surface channel and strained buried channel MOSFET devices

PTO-2040  
12/99

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ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
438	287	438	300	305			
INTERNATIONAL CLASSIFICATION							
H01L	21/336						

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	10/2	18	Fig 1A	25	1/13, 20
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Walter L. Lindsay Jr. 1/25/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	John F. Niebling Supervisory Patent Examiner Technology Center 2801 (Primary Examiner) 1/24/03 (Date)			1/28/03 ISSUE FEE <i>in</i>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	C. Mollen 2/5/03 (Legal Instruments Examiner) (Date)			Amount Due 1600	Date Paid 4-23-03
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